Kevin Stine

ECE 272

Lab 2 Study Questions

2. The lab was pretty straight forward, however I forgot to put an inverter on the inputs which made the made the switches active high instead of active low. I had to go through again and replace the buffers with inverters so it would make the switches active low and it worked perfectly.

3. Using a CPLD is a much faster method and easier to implement then the 7400-series logic chips that were used in section 1. Using a computer to write design digital logic can be a lot easier than having to use resistors and wire everything up to a power source and ground. Using a computer to do these tasks is much faster and easier. However since you must program the CPLD, it creates an issue if you do not have the correct program to program the CPLD.